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ESTD: 1929

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Dear Participants,

We are pleased to invite you to the One Week Workshop on 'Hurdles and Horizons in the Indian Semiconductor IC Design,' organized by the Department of Electronics and Communication Engineering, University College of Engineering, Osmania University, Hyderabad, in collaboration with the IEEE CAS/EDS Joint Chapter, IEEE Hyderabad Section, and the MeitY C2S Programme, Telangana State Council of Higher Education. The workshop is scheduled to be held from 21st to 26th October 2024.

This workshop is designed to bring together professionals, researchers, and students from various domains to discuss and explore the latest advancements and challenges in the field of Semiconductor IC Design. The event will feature keynote addresses, invited talks, and interactive sessions on state-of-the-art technologies, EDA tools, and hands on sessions. Additionally, it will serve as a platform for networking and collaboration among industry experts, academicians, and researchers, promoting innovation and knowledge-sharing in VLSI Technology.

Please note that registrations for the workshop are limited and will be on a first-come, first-served basis. We encourage you to confirm your participation at your earliest convenience to secure your spot.

Your participation in this workshop is highly valued, and we believe it will contribute significantly to the success of the event. We look forward to your active engagement in the discussions and activities that will take place during the workshop.

Yours sincerely,

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